

0610.68726

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kizaki et al.
Serial No.: 10/706,664
Conf. No.: 8620
Filed: 11/12/2003
For: SEMICONDUCTOR INTEGRATED
CIRCUIT
Art Unit: 2816
Examiner: Zweizig, Jeffery Shawn

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

1/4/05 Joseph P. Fox
Date Registration No. 41,760
F-CLASS.WCM
Appr. February 20, 1998 Attorney for Applicant(s)

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Election/Restriction Requirement mailed December 9, 2004,

Applicants elect Group I (Claims 1-8), without traverse.

Respectfully submitted,

GREER, BURNS & CRAIN, LTD.

January 4, 2005

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By Joseph P. Fox
Joseph P. Fox
Registration No. 41,760